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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/346,097 01/17/2003 PAT 6,762,969

yes ✓

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-030189 02/07/2002

yes ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/17/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 15	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>V. Nguyen</i> Initials				

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## TITLE

Semiconductor integrated circuit and method of manufacturing of semiconductor integrated circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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